IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Eliyahou Harari

Title:

Flash Memory Cell Arrays Having Dual Control Gates Per Memory Cell

Charge Storage Element

Application No.:

10/791,486

Filing Date:

March 1, 2004

Examiner:

Nguyen, Viet Q.

Group Art Unit:

2827

Docket No.:

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1878

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Mail Stop Amendment Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE TO REQUIREMENT FOR RESTRICTION

Sir:

In response to the non-final Office Action dated June 20, 2005 in the above-identified patent application, claims 15-30 of Group 2 are hereby elected for examination.

Respectfully submitted,

Gerald P. Parsons

Date

June 29, 2005

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